

**REMARKS**

Claims 1-8 are pending in the present application. Claims 1-8 have been rejected.

Claim 1 is amended by this Amendment. Support for this Amendment is found generally throughout the specification and specifically on page 8 lines 1-5. No new matter has been added. Accordingly, Claims 1-8 remain pending in the present application. Reconsideration is respectfully requested in view of the amendments to the claims and the following remarks.

**Claim Rejections – 35 U.S.C. §112**

Applicant respectfully submits that Claim 1 was amended to remove the text: "media access controller to provide communication with the network server over a network, including downloading information for a debugging session." Accordingly, Applicant believes that the 35 U.S.C. §112 rejection is overcome.

**Claim Rejections – 35 U.S.C. §103**

*Claims 1-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shen et al. (U.S. Pat. 6829751), hereinafter Shen.*

*As to Independent Claim 1:*

Applicant respectfully disagrees and respectfully traverses the rejection. Applicant submits Shen does not, alone or in combination, disclose, teach or suggest "...storage logic operable to store a state of the selected ones of the plurality of internal signals that match the trigger pattern for later retrieval by the network server; wherein the trigger pattern is based on software values wherein the debug client function manages an instruction pointer logic within the ASIC and to observe and manipulate registers and memory." As recited in amended Claim 1.

By contrast, Shen discloses the use of both a scan chain and a step mode configuration in its debugging configuration. More specifically, Shen's system 100 "may provide a chip diagnostics architecture by implementing the FPGA core...wherein the FPGA core 116 collects data from the registers 120a-120n and 122a-122n using a scan chain, while the system 100 is running under predetermined conditions (e.g. a step mode configuration) controlled by the FPGA core 116." (Column3, lines 37-42).

The present invention provides that the triggers are based on software values, and further depend on the debug client function's ability to manage the instruction pointer logic within the ASIC (Page 8, lines 1-5, Specification). These features in cooperation with the other claimed elements of the present invention are not disclosed by Shen. Therefore, Applicant respectfully submits that Claim 1 is allowable. As Claims 2-8 depend from Claim 1, Applicant believes such claims are also allowable for at least the reasons that apply to Claim 1.

**CONCLUSION**

Accordingly, Applicant respectfully requests reconsideration and allowance of all pending Claims 1-8 as they are newly amended and presented. Applicants' attorney believes this application is in condition for allowance. Should any unresolved issues remain, Examiner is invited to call Applicants' attorney at the telephone number indicated below.

Respectfully submitted,

January 13, 2009

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